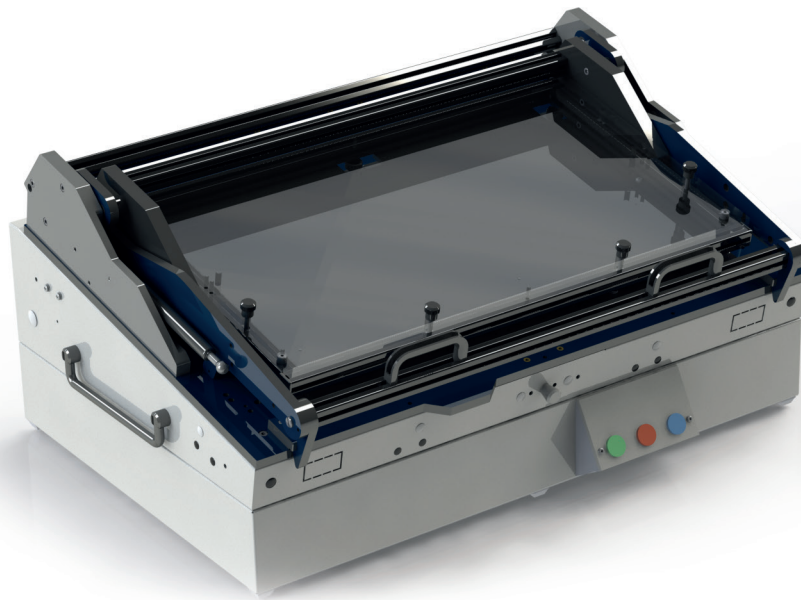


# In-System Programming (ISP) Console

Compact and Flexible

**Max PCB size:** 590x295 mm

**Max Test Points:** 200



The programming of In-Circuit or In-System (ISP) devices eliminates limitations associated with traditional programmable devices (On-Socket or Pre-Programmed). ISP delivers benefits to In-Board and System Level design, manufacturing and programming processes.

- › Fast and ergonomic fixture exchange
- › Customizable to all type of DUTs and instrumentation Interface
- › Detection of mechanical interlocking
- › Small footprint – other modules may be pressed against both sides
- › Lateral actuation for DUT connectors (USB, Ethernet, etc.) with controlled insertion force
- › Integration with Boundary Scan and functional tests



## Features

- Mechanical adaptor with parallel closing pressure mechanism
- External dimensions: 740 x 510 x 360 mm
- Possibility of individual expansion due to modular system
- Quick change of contact conveyor and retaining plate
- Interface via 4 x 96 pin VG strips, 384 standard test points
- The adaptor was designed for about 300 N (200 needles with spring force of 1.5 N)
- High operational comfort due to ergonomic design
- Custom Interface
- The box can be opened for debugging
- The adapter is supplied without the removable board set
- Wide range of accessories

### PCB Under Test Specification

Max. TP (bottom+top)	200
Max. probe force	1.5 N
Max. PCB component clearance bottom side	50 mm
Max. PCB component clearance top side	35 mm
Min. PCB border without components	2.5 mm
Max. PCB length	500 mm
Max. PCB width	300 mm
PCB thickness	0.8 to 3 mm

### Mechanical Specifications

Length	740 mm
Width	510 mm
Height	360 mm
Weight	+/- 20 Kg
Transportation height	360 + 40 mm
Electrical requirements	230V AC 50-60 Hz

#### Dedicated Flash Stations

- Offline
- Gang programming

#### Dedicated Programmers vs Universal Programmers

Eiit is official distributor of the SMH - FlashRunner ([www.smh-tech.com](http://www.smh-tech.com))

## Main Applications

Programming of flash memories integrated in ICT Test, Functional Test or Boundary Scan.

Rear



Side

